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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1 Attorney Docket Number NL 021080

NON PATENT LITERATURE DOCUMENTS

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/J.P./		J. SAVOJ, ET. AL. Design of Half-Rate Clock and Data Recovery Circuits fro Optical Communication Systems; vol. CONF. 38, June 2001; pages 121-126 USA	
/J.P./		CHAN GEUN YOON ET. AL.; Digital Logic Implementation of the Quadricorrelators for Frequency Detector; vol. 2 SYMP 37, August 1994; pages 757-760.	

Examiner Signature	/James Perez/	Date Considered	01/04/2008
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PTO/SB/08A (08-03)

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Sheet

of 1

Application Number	10/533,507
Filing Date	05/02/2005
First Named Inventor	M.A.T.Sanduleanu, et
Art Unit	2631
Examiner Name	
Attorney Docket Number	NL 021080

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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James Berg

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